



SRI VENKATESWARA COLLEGE OF ENGINEERING
(Autonomous)
TIME TABLE
 (Applicable for I Year UG & PG)

FT/GN/65C/00/20.07.16

With effect from: **18.12.2017**

Revision No: 00

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

B.E / B.Tech / M.E / M.Tech / Regulation: 2016

PG Specialisation: POWER ELECTRONICS & DRIVES


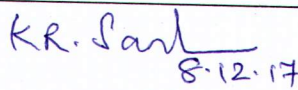

Academic Year: 2017 -18

Semester: ~~ODD~~/EVEN

DAY	Semester: IV			Section: -	Class Room: 3208		Block: EE	
	1 8.30 - 9.20	2 9.20 - 10.10	3 10.10-11.00		4 11.40-12.30	5 12.30- 1.20		6 1.35-2.25
MON	PD16411 - Project Work			L U N C H B R E A K	PD16411 - Project Work		B R E A K	PD16111 - Project Work
TUE	PD16411 - Project Work				PD16411 - Project Work			PD16411 - Project Work
WED	PD16411 - Project Work				PD16411 - Project Work			PD16411 - Project Work
THU	PD16411 - Project Work				PD16411 - Project Work			PD16411 - Project Work
FRI	PD16411 - Project Work				PD16411 - Project Work			PD16411 - Project Work

SUBCODE	SUBJECT NAME	CREDITS	NAME OF THE FACULTY	FACULTY DEPT
PD16411	Project Work (Phase II)	12	Dr. C GOPINATH	EE

FACULTY ADVISERS	ROLL NO	STUDENT REPRESENTATIVES	ROLL NO
Mr. T ANNAMALAI	1 - 6	SUNITHA T	05
CLASS COMMITTEE CHAIRPERSON		Dr. NALIN KANT MOHANTY	

 Dept. TTC	 HOD	 Principal
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