



SRI VENKATESWARA COLLEGE OF ENGINEERING
(Autonomous)
TIME TABLE
 Applicable for UG – II Year

FT/GN/65D/00/21.04.17

With effect from: **25/03/2021**

Revision No: 01

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

B.E Regulation: 2018

Academic Year: 2020-21

Semester: EVEN

Semester: IV

Section:A


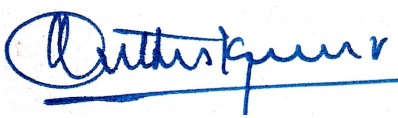

Class Room: ON LINE

Block:

DAY	1	2	3	4	L U N C H B R E A K	5	6	7
	08.30-09.20	09.30-10.20	10.30-11.20	11.30-12.20		01.10-02.00	02.10-03.00	03.10-04.00
MON	EC18402	EC18401	EC18404	MA18454		EC18405		
TUE	EC18401	EC18405	EC18402			MA18454	EC18403	
WED	EC18403	EC18404	EC18405	EC18402			EC18401	
THU	MA18454		EC18401	EC18404		EC18403	EC18402	
FRI	EC18405	EC18403	MA18454			EC18404		

SUB. CODE	SUBJECT NAME	CREDITS	NAME OF THE FACULTY	FACULTY DEPT.
EC18401	Analog Communication Systems	3	Mr.P.Muthukumaran	ECE
EC18402	Signals and Systems	3	Ms.M.Vidya	ECE
EC18403	Analog Integrated Circuits and its Applications	3	Mr.K.Venkatesh	ECE
EC18404	Linear Control Systems	3	Ms.Rajeswari Ramaraj	ECE
EC18405	Microprocessor and Microcontroller Based System Design	3	Ms.S.M.Mehzabeen	ECE
MA18454	Probability and Random Processes	4	Dr.V.Valliammal	APM

FACULTY ADVISERS	ROLL NO	STUDENT REPRESENTATIVES	ROLL NO
Ms.B.Sarala Dr.G.A.Sathish Kumar		Mr.J.Bharathi Krishnaa Ms. Haripriya S	12 35
CLASS COMMITTEE CHAIR PERSON		Dr. N.Kumarathan	

 Dept. TTC	 HOD	 PRINCIPAL
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