



**SRI VENKATESWARA COLLEGE OF ENGINEERING**  
(Autonomous)  
TIME TABLE  
Applicable for UG – III Year

FT/GN/65B/00/21.04.17

With effect from: 09/08/2021

Revision No: 01

**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**  
B.E Regulation: 2018

DAY	Section: A				L U N C H  B R E A K	Semester: ODD		Block:
	1	2	3	4		5	6	
	08.30-09.20	09.30-10.20	10.30-11.20	11.30-12.20		01.10-02.00	02.10-03.00	03.10.04.00
MON	EC18504	GE18551	EC18501	EC18502		EC18503		
TUE	EC18009/ EC18013	EC18503	EC18502			EC18504	GE18551	
WED	EC18501			EC18009/ EC18013		EC18502		
THU	EC18502	EC18504	EC18503	EC18501		EC18009/ EC18013		
FRI	GE18551		EC18504	EC18503		EC18501		
SAT		EC18009/ EC18013		GE18551				

SUB. CODE	SUBJECT NAME	CREDITS	NAME OF THE FACULTY	FACULTY DEPT.
EC18501	Digital Communication	4	Dr.S.R.Malathi	ECE
EC18502	Principles of Digital Signal Processing	4	Ms.B.Sarala	ECE
EC18503	Computer Organization and Design	4	Mr.P.Arul	ECE
EC18504	Transmission Lines and Waveguides	4	Ms.K.Srividhya	ECE
GE18551	Principles of Management (Common to EC,AE, BT, EE and ME)	3	Dr.S.Vijayanand	ECE
EC18009/ EC18013	Medical Electronics/ Robotics and Automation (PE-1)	3	Dr.S.Velusamy/ Mr.S.P.Sivagnana Subramanian	ECE

FACULTY ADVISERS	ROLL NO	STUDENT REPRESENTATIVES	ROLL NO
Ms.B.Sarala Dr.S.Vijayanand		Mr.J.Bharathi Krishnaa Ms. Haripriya S	12 35
CLASS COMMITTEE CHAIR PERSON		Dr.P.Jothilakshmi	

 Dept. TTC	 HOD	 PRINCIPAL
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