



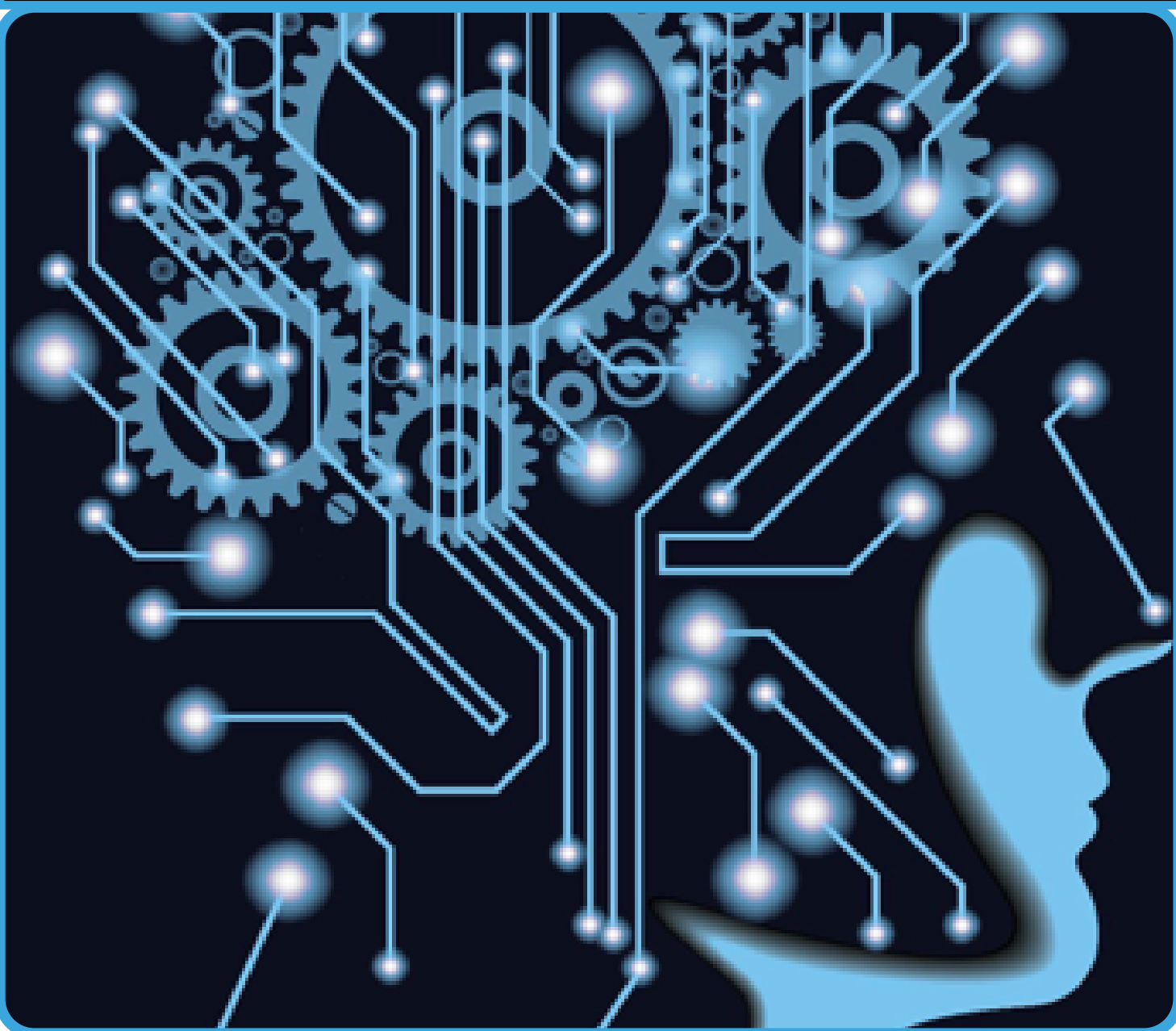
Sri Venkateswara College of Engineering

An Autonomous Institution - Affiliated to Anna University
Sriperumbudur Tk - 602 117



Session Timing: 3pm to 4.30pm

- ▶ **E-Certificate will be provided**
- ▶ **The Event is offered at Free of cost**
- ▶ **G-form link: [Click here](#)**
- ▶ **Target audience - II and III year students of ECE**



Webinar

On

"PCB Design Flow Process"

15th August 2020

Organized by

Department of

Electronics & Communication Engineering

What Will You Learn ?



- ▶ Circuit Design and Simulation
- ▶ Schematic Layout to PCB Layout
- ▶ DRC and Net list Checking



Convenor:

Dr.S.Muthukumar

Head of the Department-ECE,

Coordinators

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Technology Expert

R.Premalatha

SVCE Alumnus

Hardware Board Designer

Join Through:



<https://meet.google.com/kvm-wvts-bad>

Registration Link:

<https://forms.gle/egFijaiRYZE71XC28>



SRI VENKATESWARA COLLEGE OF ENGINEERING (SVCE) PENNALUR,
SRIPERUMBUDUR-602117

	Department of Electronics and Communication Engineering	 INSTITUTION'S INNOVATION COUNCIL <small>(Ministry of HRD Initiative)</small>	 ISO 9001:2015 Certified by IRQS MGMT. SYS. RvA C 071
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Webinar On

"PCB Design Flow Process"

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Technology Expert

R.Premalatha
SVCE Alumnus
Hardware Board Designer

REPORT ON TECHNICAL TALK WITH Ms. PREMALATHA, HARDWARE BOARD DESIGNER -15th AUG 2020

Date: 16/8/2020

Time: 3-4.30PM

Objectives (Maximum 50 words):

To provide an insight into PCB design and develop board modelling using simulation tool.

About the programme (Min 500 words):

The session started with the significance of VLSI and PCB Engineering in our daily life and the methods involved in every popular design. It later continued with illustration of

the processes using a flowchart and the significance of each and every steps and techniques behind them. Live demo of the modeling and development ,including component placement,routing,analysis of parameters,connectivity check,etc was done using a ORCAD PSPICE EDA tool box with explanation of the development of board, behind modeling and simulation.It helped students to visualize board level design as well got to know the calculations as well. The workshop proved a very efficient learning platform with enjoyable learning and was a grand success.

Benefits (Maximum 50 words):

It provided learning by modelling using a simulation platform which anyone could work with it.It provided more technical as well an enjoyable learning.Around 45 students from ECE department attended the event and got benefitted.

Prepared by Faculty Name, Designation & Dept.

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S.Kalyani, Asst.Prof/ECE L.Anju,
Asst.Prof/ECE M.K.Varadarajan,
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SIGNATURE OF THE ORGANISER



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Webinar on PCB design flow process

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