



Department of Information Technology			<b>LP: CS16502</b> <b>Rev. No: 00</b> <b>Date:</b> 29.06.2018
B.E/B.Tech	: CS/IT (Autonomous)	Regulation: 2016	
Sub. Code / Sub. Name	: CS16502 / Object Oriented Analysis and Design		
Unit	: I		

### Unit Syllabus: INTRODUCTION & INCEPTION

Object-Oriented Analysis and Design - Iterative, Evolutionary, and Agile : Unified Process, Iterative and Evolutionary Development, Waterfall Lifecycle, How to do Iterative and Evolutionary Analysis and Design, Agile Methods and Attitudes, Agile Modeling, Agile UP, UP Phases, UP Disciplines - Case Studies : The NextGen POS System, The Monopoly Game System - Inception is Not the Requirements Phase - Evolutionary Requirements - Use Cases – Relating Use Cases - Other Requirements

#### Objective:

This unit explains the basics of OO Analysis and Design skills.

Session No *	Topics to be covered	Ref	Teaching Aids
1	<b>Object-Oriented Analysis and Design:</b> Basics, Learning goals, Examples for various types of models	T1, Ch 1, pg(3-14)	BB/LCD
2	<b>Iterative, Evolutionary, and Agile :</b> Unified Process, Iterative and Evolutionary Development, Waterfall	T1, Ch 2, pg(17-27)	BB/LCD
3	<b>Iterative, Evolutionary, and Agile :</b> Agile Methods and Attitudes, Agile Modeling, Agile UP, UP Phases, UP Disciplines	T1, Ch 2, pg(27-39)	BB/LCD
4	<b>Case Studies :</b> The NextGen POS System, The Monopoly Game System	T1, Ch 3, pg(41-44)	BB/LCD
5	<b>Inception is Not the Requirements Phase:</b> What is inception? – How long is inception?	T1, Ch 4, pg(47-51)	BB/LCD
6	<b>Evolutionary Requirements:</b> Evolutionary vs. waterfall requirements – types and categories of requirements	T1, Ch 5, pg(53-59)	BB/LCD
7	<b>Use Cases, Relating Use Cases:</b> Introduction to usecases, Actors, Scenarios	T1, Ch 6, pg(61-64)	BB/LCD
8	<b>Use Cases, Relating Use Cases:</b> usecase model, Kinds of actors and usecases, Guidelines for naming actors and usecases	T1, Ch 6, pg(64-99)	BB/LCD
9	<b>Other Requirements</b>	T1, Ch 7, pg(101-119)	BB/LCD
Content beyond syllabus covered (if any):			

\* Session duration: 50 minutes



**Sub. Code / Sub. Name : CS16502 / Object Oriented Analysis and Design**

**Unit : II**

### Unit Syllabus: ELABORATION – BASICS

Domain Models - System Sequence Diagrams - Operation Contracts - Logical Architecture and UML Package Diagrams - UML Interaction Diagrams - UML Class Diagrams - Designing for Visibility - Refactoring.

#### Objective:

This unit deals with the UML diagrams for Modeling.

Session No *	Topics to be covered	Ref	Teaching Aids
10	<b>Domain Models</b> – Finding conceptual classes and descriptive classes	T1, Ch 9, pg(131-149)	BB/LCD
11	<b>Domain Models</b> – Association and attributes	T1, Ch 9, pg(149-170)	BB/LCD
12	<b>System Sequence Diagrams</b> – Relationship between SSDs and use cases	T1, Ch 10, pg(173-180)	BB/LCD
13	<b>Operation Contracts</b> – OCL	T1, Ch 11, pg(181-194)	BB/LCD
14	<b>Logical Architecture and UML Package Diagrams</b> – Software architecture, Model – view separation	T1, Ch 13, pg(197-212)	BB/LCD
15	<b>UML Interaction Diagrams</b> - Sequence and communication diagram	T1, Ch 15, pg(221-247)	BB/LCD
16	<b>UML Class Diagrams</b> - Introduction, Notation, designing class diagram, Classifier, Operations and	T1, Ch 16, pg(249-260)	BB/LCD
17	<b>UML Class Diagrams</b> - Dependency and interfaces, Composition, Aggregation, Constraints, Association	T1, Ch 16, pg(260-270)	BB/LCD
18	<b>Designing for Visibility, Refactoring</b> - Types of visibility and examples	T1, Ch 19, pg(363-368)	BB/LCD
<b>Content beyond syllabus covered (if any):</b>			

\* Session duration: 50 mins



**Sub. Code / Sub. Name : CS16502 / Object Oriented Analysis and Design**

**Unit : III**

### Unit syllabus: ELABORATION - DESIGN PATTERNS

GRASP: Designing Objects with Responsibilities, Polymorphism, Pure Fabrication - Object Design Examples with GRASP : What is a Use Case Realization, Use Case Realizations for the NextGen Iteration - Applying GoF Design Patterns : Adapter, Factory, Singleton (GoF), Strategy (GoF), Composite (GoF), Facade (GoF), Observer/Publish-Subscribe/Delegation Event Model.

### Objective

In this unit, students will learn the design patterns

Session No *	Topics to be covered	Ref	Teaching Aids
19	<b>GRASP: Designing Objects with Responsibilities – Creator, Information Expert, Low Coupling, High</b>	T1, Ch 17, pg(271-290)	BB/LCD
20	<b>GRASP: Designing Objects with Responsibilities – Creator, Information Expert</b>	T1, Ch 17, pg(291-299)	BB/LCD
21	<b>GRASP: Designing Objects with Responsibilities – Low Coupling, High Cohesion, Controller</b>	T1, Ch 17, pg(299-318)	BB/LCD
22	<b>GRASP: Polymorphism, Pure Fabrication</b>	T1, Ch 25, pg(413-426)	BB/LCD
23	<b>Object Design Examples with GRASP: What is a Use Case Realization, Use Case Realizations for the</b>	T1, Ch 18, pg(321-349)	BB/LCD
24	<b>Applying GoF Design Patterns : Adapter, Factory</b>	T1, Ch 26, pg(435-442)	BB/LCD
25	<b>Applying GoF Design Patterns: Singleton (GoF), Strategy (GoF).</b>	T1, Ch 26, pg(442-452)	BB/LCD
26	<b>Applying GoF Design Patterns : Composite (GoF), Facade (GoF)</b>	T1, Ch 26, pg(452-463)	BB/LCD
27	<b>Applying GoF Design Patterns: Observer/Publish-Subscribe/Delegation Event Model.</b>	T1, Ch 26, pg(463-471)	BB/LCD

**Content beyond syllabus covered (if any):**

\* Session duration: 50 mins



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**Unit : IV**

**Unit syllabus: ELABORATION – DYNAMIC MODELING**

UML Activity Diagrams and Modeling - UML State Machine Diagrams and Modeling - Domain Model Refinement - Logical Architecture Refinement - Designing a Persistence Framework with Patterns - UML Deployment and Component Diagrams

**Objective**

In this unit, students will learn the layered architecture

Session No *	Topics to be covered	Ref	Teaching Aids
28	<b>UML Activity Diagrams and Modeling</b>	T1, Ch 28, pg(477-483)	BB/LCD
29	<b>UML State Machine Diagrams and Modeling</b>	T1, Ch 29, pg(485-491)	BB/LCD
30	<b>Domain Model Refinement</b> – Finding conceptual class, class hierarchies, aggregation and composition	T1, Ch 31, pg(501-516)	BB/LCD
31	<b>Domain Model Refinement</b> – Aggregation and Composition	T1, Ch 31, pg(519-522)	BB/LCD
32	<b>Domain Model Refinement</b> – Association role names, qualified associations	T1, Ch 31, pg(522-526)	BB/LCD
33	<b>Logical Architecture Refinement</b> – Collaboration with the layers pattern, Model – View separation and upward	T1, Ch 34, pg(559-576)	BB/LCD
34	<b>Designing a Persistence Framework with Patterns</b>	T1, Ch 37, pg(621-649)	BB/LCD
35	<b>Designing a Persistence Framework with Patterns</b>	T1, Ch 37, pg(621-649)	BB/LCD
36	<b>UML Deployment and Component Diagrams</b>	T1, Ch 38, pg(651-653)	BB/LCD

**Content beyond syllabus covered (if any):**

\* Session duration: 50 mins



**Sub. Code / Sub. Name : CS16502 / Object Oriented Analysis and Design**

**Unit : V**

### Unit syllabus : OBJECT ORIENTED TESTING

Mapping design to code – Testing: Issues in OO Testing – Class Testing – OO Integration Testing – GUI Testing – OO System Testing.

#### Objective

- Learn to map, design to code
- Be exposed to the various testing techniques

Session No *	Topics to be covered	Ref	Teaching Aids
37	<b>Mapping design to code</b> - Creating methods, collection classes in code	T1, Ch 20, pg(369-380)	BB/LCD
38	<b>Mapping design to code</b> – exceptions and error handling	T1, Ch 20, pg(369-380)	BB/LCD
39	<b>Testing</b> : Issues in OO Testing – Units for OO Testing, implication of composition and encapsulation,	T2, Ch 16, pg(285-290)	BB/LCD
40	<b>Class Testing</b> – methods as units, pseudocode, classes as units	T2, Ch 17, pg(297-305)	BB/LCD
41	<b>OO Integration Testing</b> – MM – paths for OO software, framework	T2, Ch 18, pg(311-323)	BB/LCD
42	<b>OO Integration Testing</b> – framework	T2, Ch 13, pg(424-426)	BB/LCD
43	<b>GUI Testing</b> – Unit testing, Integration testing and System testing for Currency conversion program	T2, Ch 19, pg(327-330)	BB/LCD
44	<b>OO System Testing</b> – Currency convertor, UML based system testing	T2, Ch 19, pg(337-349)	BB/LCD
45	<b>OO System Testing</b> – State chart based system testing	T2, Ch 20, pg(337-349)	BB/LCD
<b>Content beyond syllabus covered (if any):</b>			

\* Session duration: 50 mins



## SRI VENKATESWARA COLLEGE OF ENGINEERING

## COURSE DELIVERY PLAN - THEORY

	Prepared by	Approved by
Signature	<i>S. Uvarajan</i>	<i>[Signature]</i>
Name	<b>Mr S Uvaraj Arutkumaran</b>	<b>Dr. V.Vidhya</b>
Designation	<b>Assistant Professor/IT</b>	<b>HOD Incharge/IT</b>
Date	29.06.2018	29.06.2018
Remarks *:		
Remarks *:		

\* If the same lesson plan is followed in the subsequent semester/year it should be mentioned and signed by the Faculty and the HOD