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## **DEPARTMENT OF APPLIED PHYSICS** Cordially invites you for the seminar on

# ADVANCED SEMICONDUCTOR DEVICES

## Er. SURYA VISWANATHAN B.E., M.S., Senior ESD devices and modeling engineer

globalfoundaries

Woodland, Singapore

Scheduled on : 04/05/2022 Timing : 6:30pm-7:30pm (IST)

## GOOGLE MEET LINK:

https://meet.google.com/ktf-gsov-foj

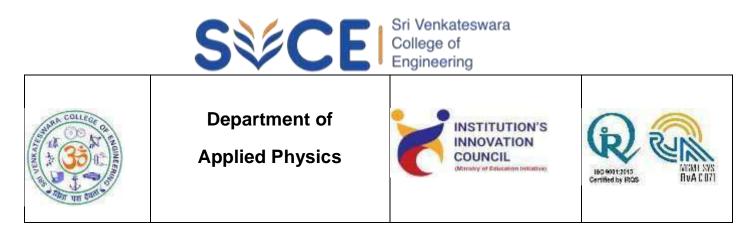
## Convener

Dr. A.Bhaskaran Proff. and Head

## Coordinators

Dr. T.Senthilnadhan Asst. Proff. Dr. S.Sampath Krishnan Proff.





### Report of the Webinar on "Advanced Semiconductor Devices"

### Date: 04/05/202

Time: 06.30 – 08.00 P.M

### **OBJECTIVES**:

The objective of the webinar was to enlighten both students and faculty members with recent trends in Semiconductor devices. In this connection a webinar entitled "Advanced Semiconductor Devices" is organized

#### **ABOUT THE WEBINAR:**

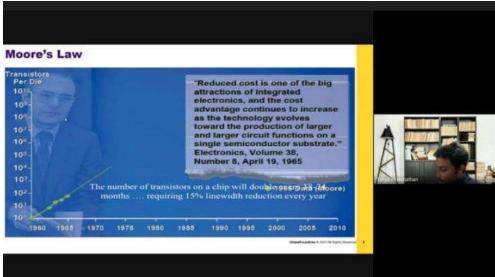
On 4<sup>th</sup> May 2022, the webinar was organized. The Speaker, **Mr.SuryaViswanathan**, B.E., M.S., is currently designated as the ESD Devices and Modeling Engineer and employed in Global Foundaris, Woodland, Singapore. He had seven years of experience with the process integration Engineers for design development. He had worked with baseband modeling teams and integrated ESD model file in to the PDK teams to the benefit of customers. He had also experienced in the operation of testers such as Keithley, Celestron, Hanwa, Keysight and oscilloscopes.

Dr. A. Bhaskaran, Professor & Head, Department of Applied Physics has delivered the welcome address and the Coordinator of the Webinar Dr.T.Senthilnathan, Assistant Professor, Department of Applied Physics has introduced about the speaker. The speaker greeted the gathering and gave a brief overview on Scope semiconductor devices design in the Electronic Industries..

The session ended with a vote of thanks which is delivered by Dr.S. Sampath Krishnan, Professor, Department of Applied Physics.

### **BENEFITS:**

The participants understood the importance of Scaling in the integrated circuits, basic device principles MOSFET, trigate transistors and CMOS fabrication sequences



### SNAPSHOTS OF THE WEBINAR





Prepared By: Dr.T.Senthilnathan Programme Coordinator